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August 2000 ACM Transactions on Computer Systems (TOCS), Volume 18 Issue 3

Full text available: pdf(287.05 KB)

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Binto George, Jayant R. Haritsa

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Keywords: Buffer management, Covert channels, Firm deadlines, Real-time database

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Pre-execution techniques have received much attention as an effective way of prefetching cache bl ever-increasing memory latency. A number of pre-execution techniques based on hardware, compil been proposed and studied extensively by researchers. They report promising results on simulators Simultaneous Multithreading (SMT) processor. In this paper, we apply the helper threading idea on multithreaded machine, i.e., Intel Pentium 4 processor with Hyp ...

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Philip Machanick

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